

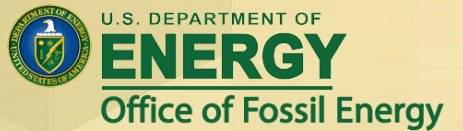
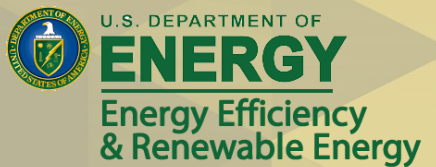


ENERGY INNOVATION

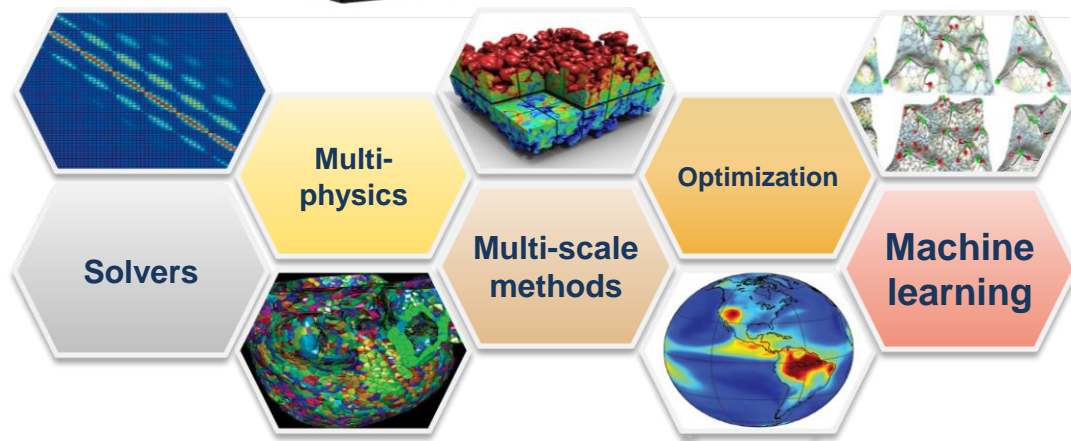


National Laboratories Partner with U.S. Industry to Increase Innovation and Energy Efficiency

Aaron Fisher
Acting Director HPC4EnergyInnovation



HPC4EI connects the DOE HPC ecosystem to US industry



World-class supercomputers
Unique software
Subject Matter Experts



HPC4EI has established an efficient process for DOE Labs to partner with industry



- Two solicitations a year
 - AMMTO, IEDO, FECM
- \$400k / Project
- ~\$6M /yr Budget
- 11 participating National laboratories
- 150+ projects funded with 90+ companies over 7 years

Industry
Concept
Paper

Identify Lab PI

Submit Full
Proposal

Technical
Review

Award

PI Secures
HPC
Resources

Execute

Visit our website for solicitation details

FOA is expected soon!

Topics

...

- Improvements in semiconductor technologies that will result in operational energy efficiency improvements, supporting achieving the goals of [EES2](#)

...

Questions can be sent to hpc4ei@llnl.gov

Join the hpc4ei-info@llnl.gov distribution lists via the web to receive program announcements

The screenshot shows the HPC4 Energy Innovation website homepage. The header includes the HPC4 Energy Innovation logo and a navigation menu with links for SOLICITATIONS, EVENTS, COMPUTING RESOURCES, SUCCESS STORIES, PROJECTS, FAQs, CONTACT, and a 'Now Accepting Applications' button. The main banner features the text 'High Performance Computing for Energy Innovation' and a tagline: 'In partnership with industry, leveraging world-class computational resources to advance the national energy agenda.' Below the banner are three main content blocks: 1) 'Webinar' section with a calendar icon, listing 'Spring 2022 Informational Webinars' on May 10, 2022 and May 18, 2022, with a 'View Details' button. 2) 'OPEN CALL' section with a yellow header, stating 'Spring 2022 solicitation will focus on topic areas associated with the HPC4Manufacturing (HPC4Mfg) Program', a 'START HERE Spring 2022 Solicitation' button, and 'up to \$3 million in funding'. 3) 'Learn More' section with an image of a tablet, 'High Performance Computing for Manufacturing' text, and a 'View Brochure' button. The footer contains links for Project Stats, How it Works, Success Stories, Sponsors, and Laboratories.

www.hpc4energyinnovation.org

Post-exascale System Architectures: Hardware overprovisioning and energy efficiency

EES2 Meeting

20 July 2023

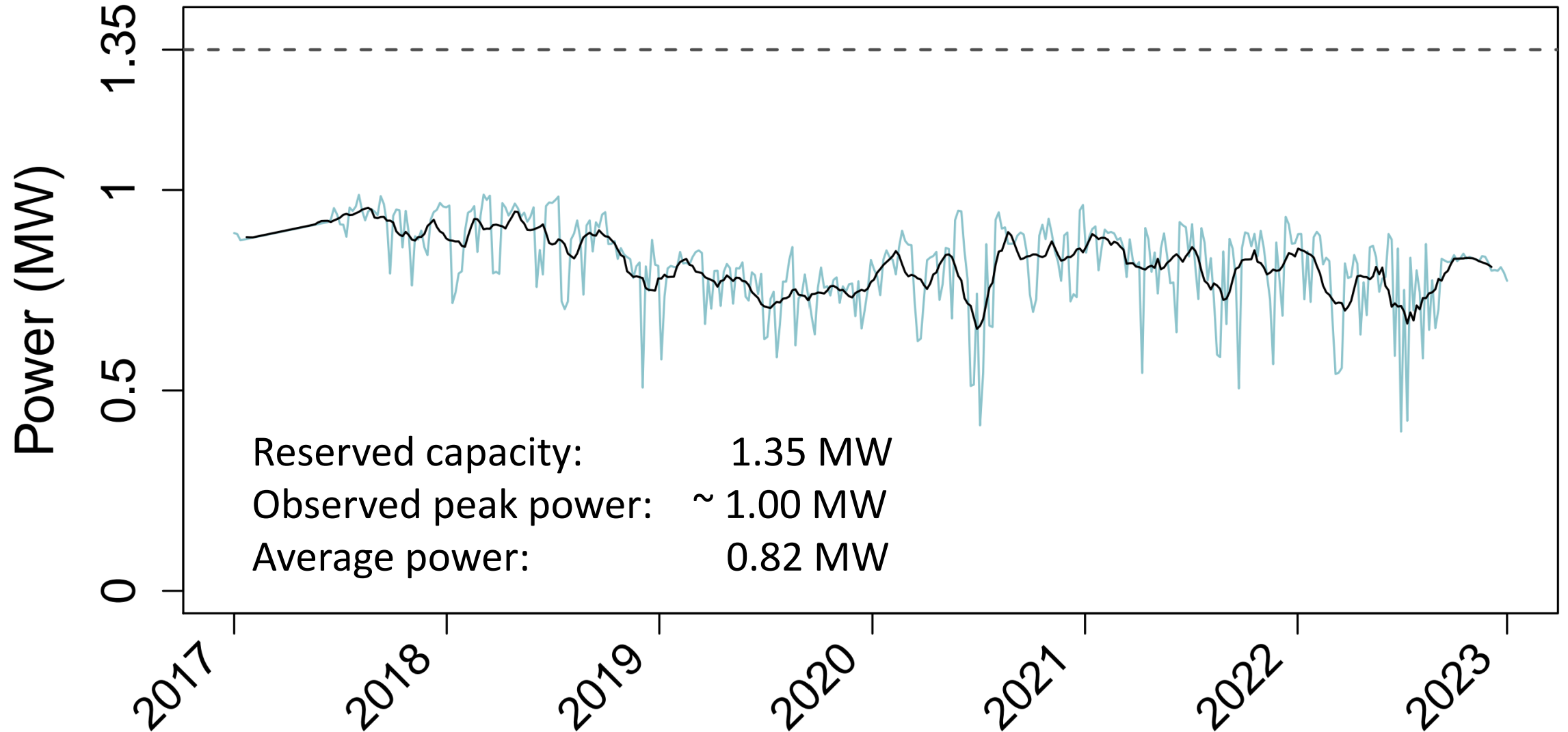
Barry Rountree and Tapasya Patki
{rountree | patki1}@llnl.gov
Center for Applied Scientific Computing



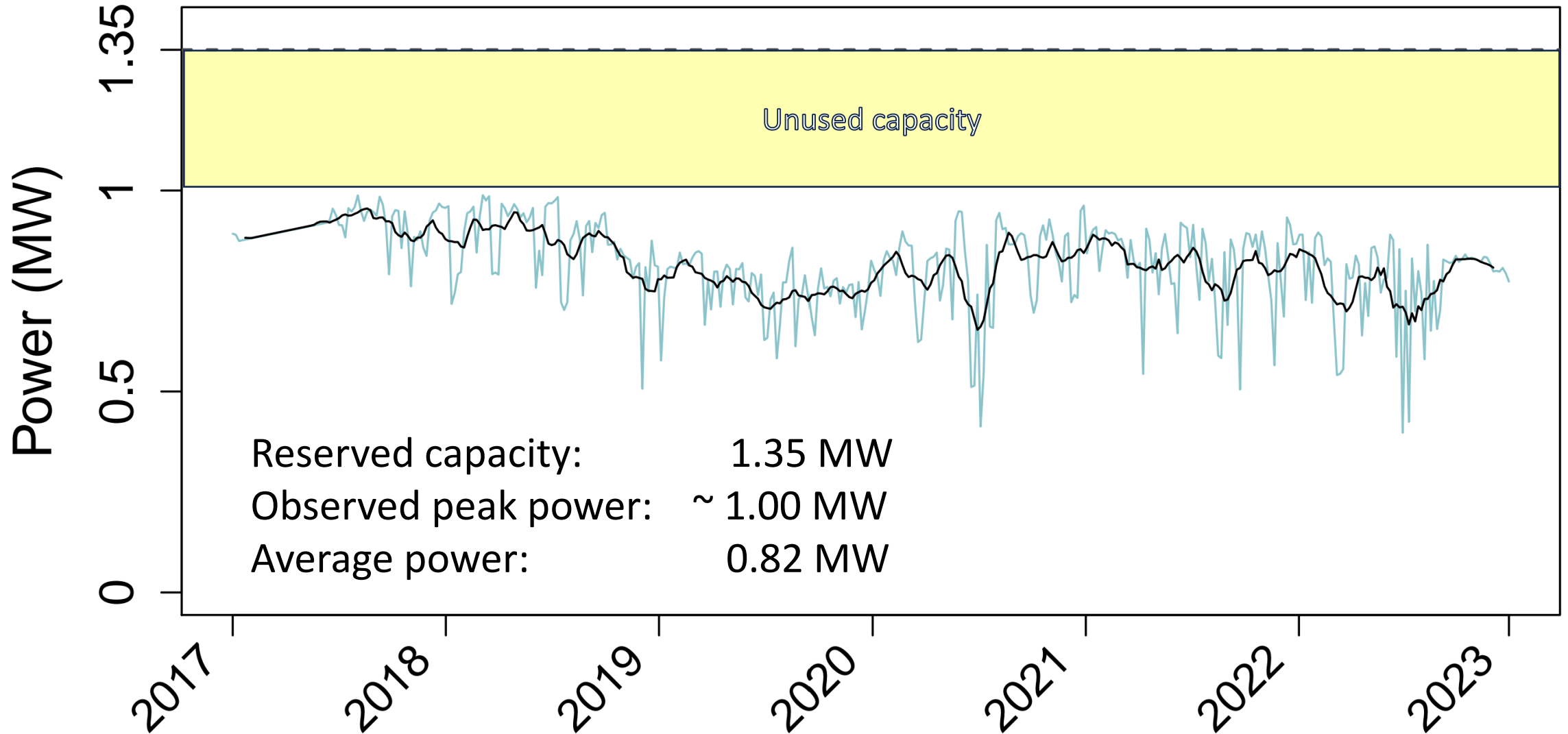
LLNL-PRES-XXXXXX

This work was performed under the auspices of the U.S. Department of Energy by Lawrence Livermore National Laboratory under contract DE-AC52-07NA27344. Lawrence Livermore National Security, LLC

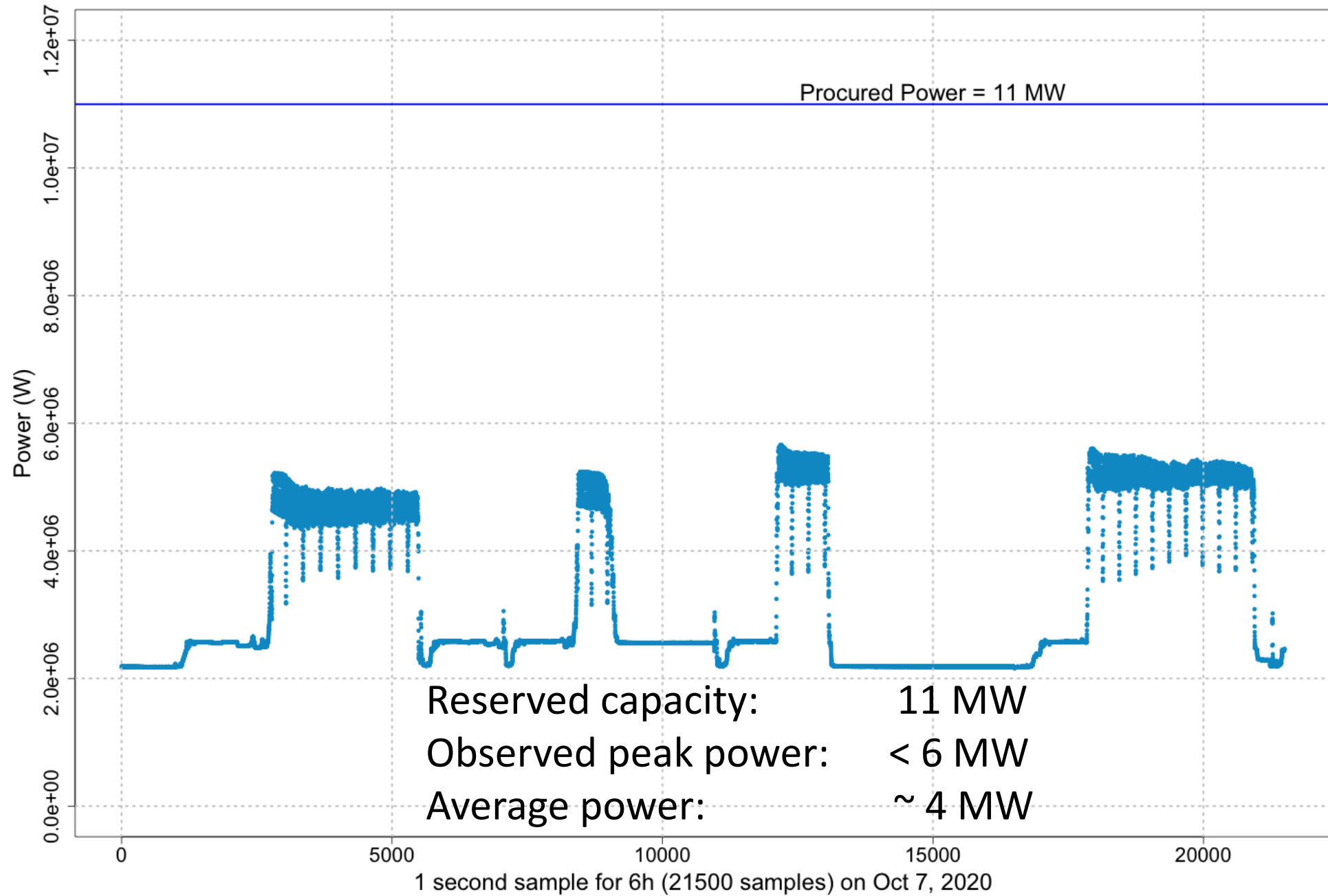
Total Power Consumption of Broadwell Quartz Supercomputer Nov 2017 to May 2023



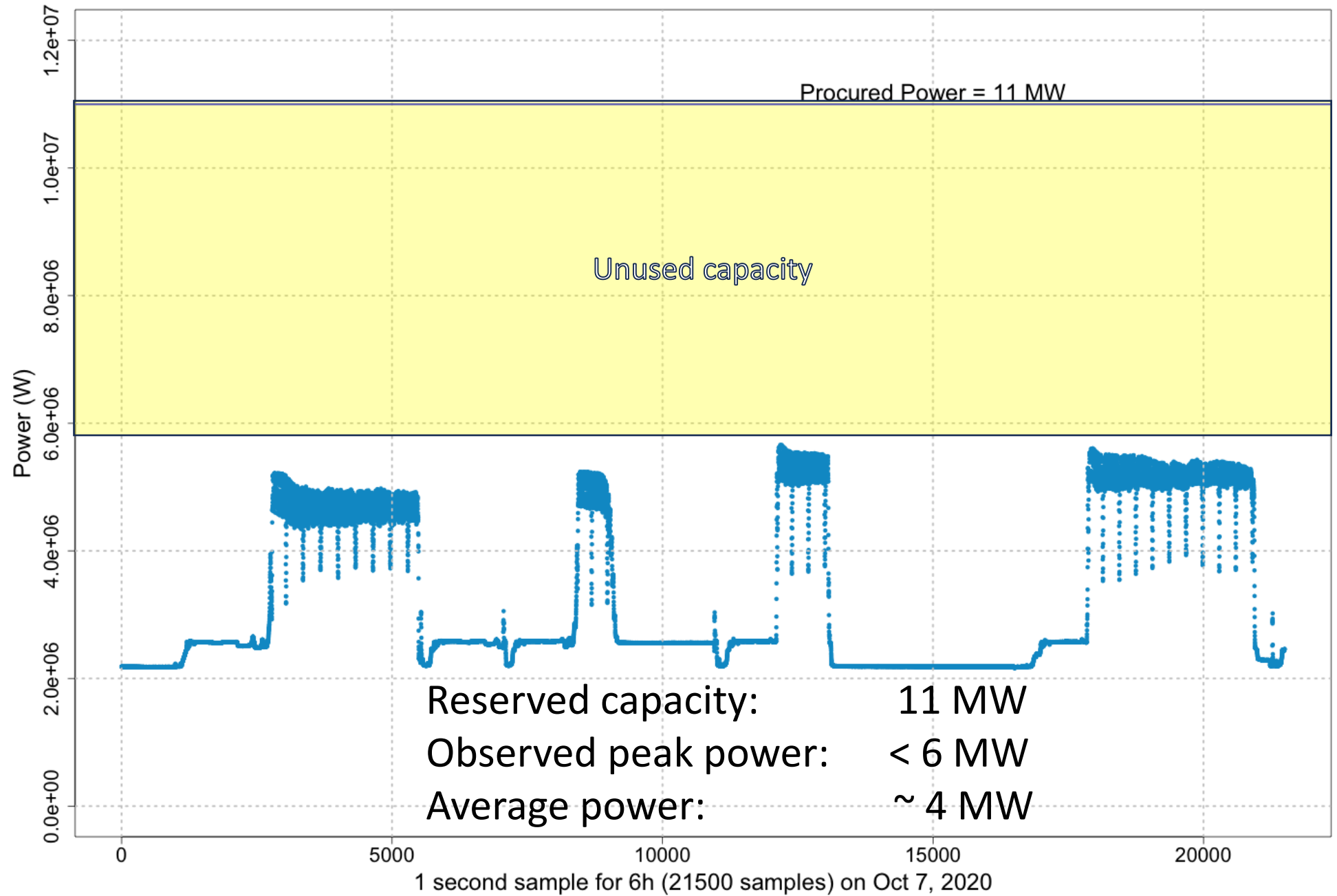
Total Power Consumption of Broadwell Quartz Supercomputer Nov 2017 to May 2023



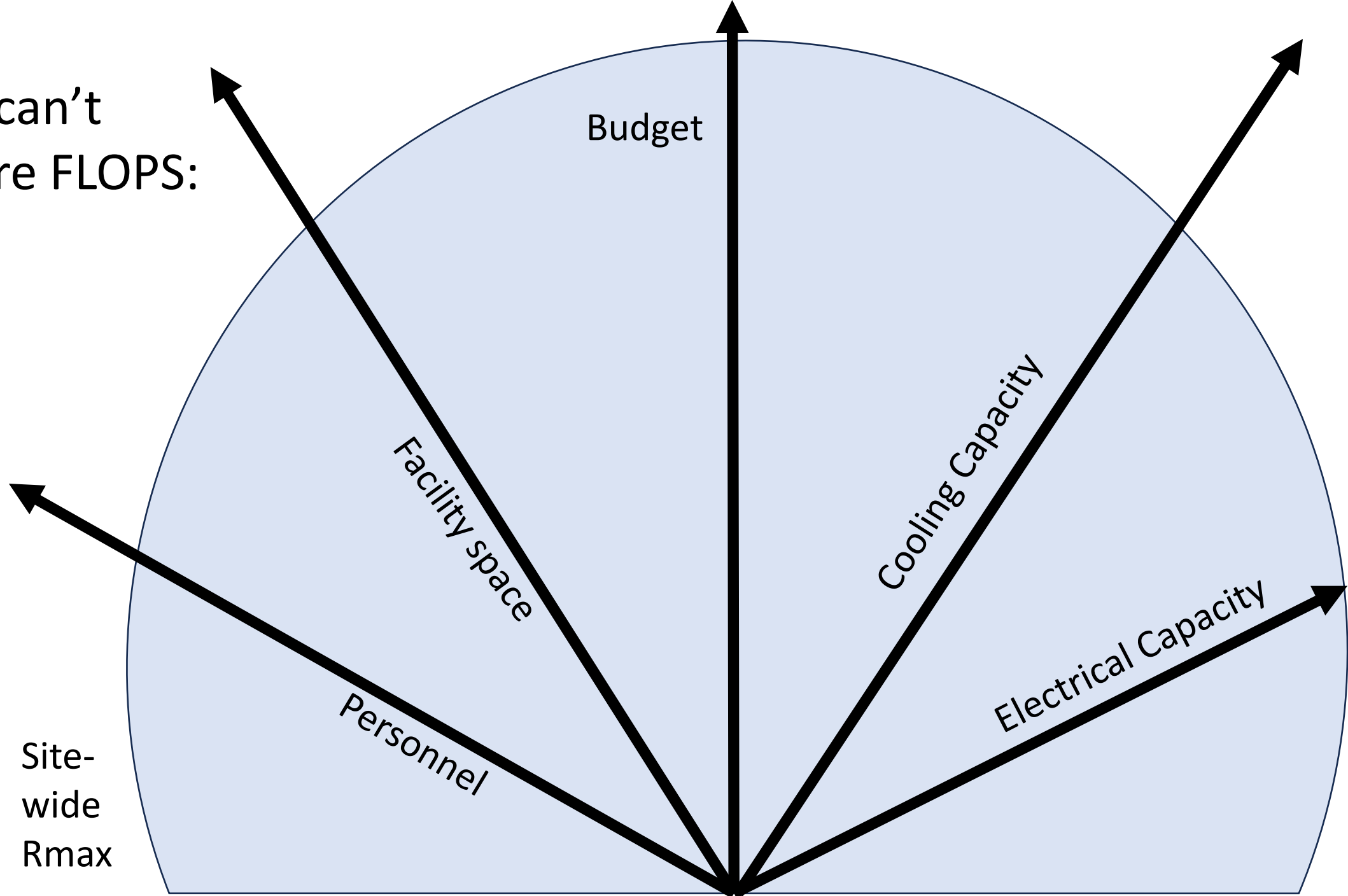
Power Swings with the Covid-19 workflow with LBANN on Sierra



Power Swings with the Covid-19 workflow with LBANN on Sierra



Why we can't
have more FLOPS:



Electrical Capacity



TDP (Thermal design point, worst-case consumption)



Current power allocation

Supercomputers in a facility have different worst-case electrical requirements

Electrical Capacity



TDP (Thermal design point, worst-case consumption)



Current power allocation



Observed peak power

Electrical Capacity



TDP (Thermal design point, worst-case consumption)



Current power allocation



Observed peak power



Observed average power

Electrical Capacity



TDP (Thermal design point, worst-case consumption)



Current power allocation



Observed peak power



Observed average power



Hardware over-provisioning with static, machine-wide power caps

New compute capacity

No energy savings

Modest efficiency increase

Electrical Capacity



TDP (Thermal design point, worst-case consumption)



Current power allocation



Observed peak power



Observed average power



New compute capacity

Hardware over-provisioning with static, machine-wide power caps

Electrical Capacity



New compute capacity

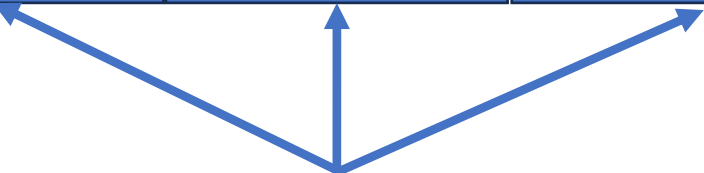
Redeployable capacity

Dynamic hardware overprovisioning allows redeployable computing

Electrical Capacity



Dynamic hardware overprovisioning allows redeployable computing



Provisioned for peak power

Electrical Capacity



Dynamic hardware overprovisioning allows redeployable computing



No energy savings



Much higher efficiency



Saved energy translated into more FLOPS, faster turnaround, higher throughput

Provisioned for peak power

NABC



- Need

- Increase compute capacity within the existing facility envelopes

- Approach

- Apply existing hardware and system controls to cap power and waste heat

- Level 1: Static, machine-wide caps
- Level 2: Dynamic machine caps, static job caps power-aware machine scheduling
- Level 3: Dynamic job caps, uniform node caps power-aware scheduling
- Level 4: Dynamic node caps power-aware runtime systems

- Benefit

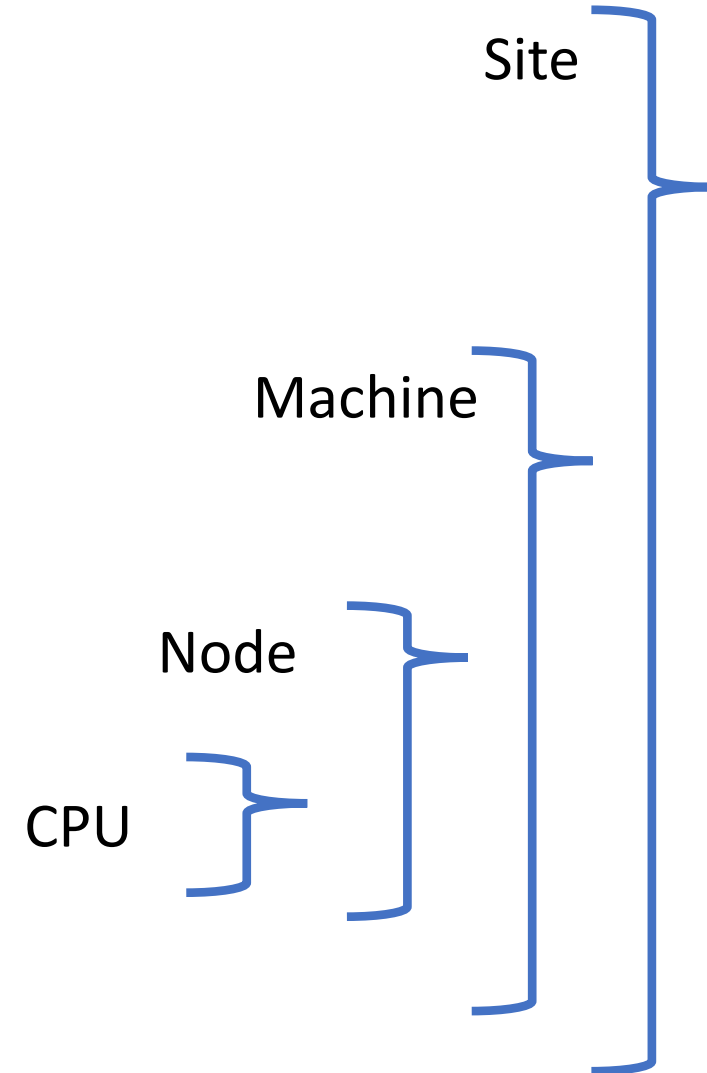
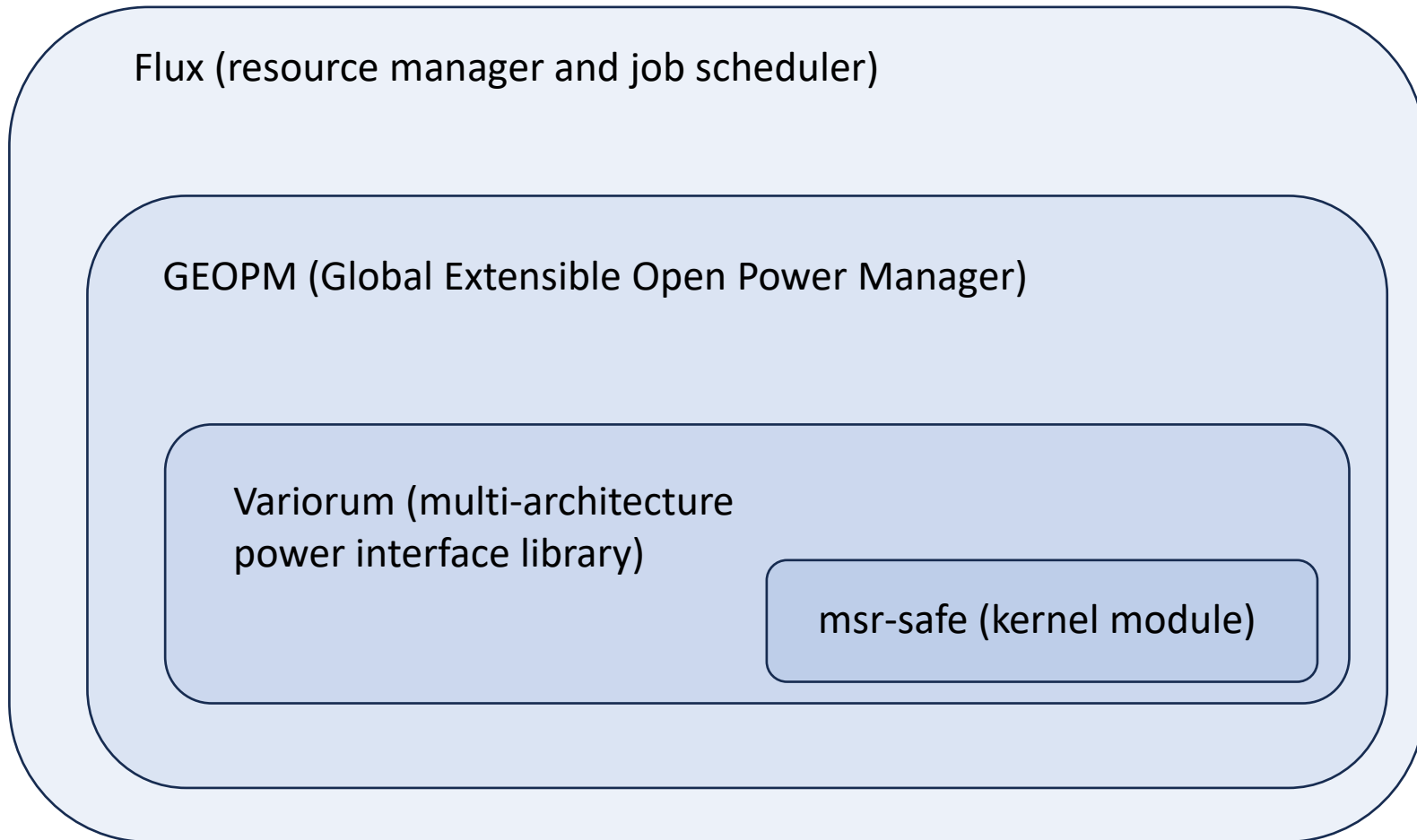
- More FLOPs, use the capacity we've already paid for

- Competition

- Cloud computing approaches

LLNL Software Stack

LDMS (Lightweight Distributed Metric Service)



Lessons Learned

- DOE NNSA labs care about energy efficiency.... to a point
 - Efficient machine design is important
 - Once the machine is delivered, run it as fast as possible
- Measure total system energy, not just CPU/GPU energy
 - Faster algorithms are (almost) always more energy efficient
- Respect diversity of optimization goals
 - Speed, turnaround time, throughput need different strategies
- Energy efficiency matters most when energy is a constraint

Open Questions

- How to simultaneously schedule jobs, energy, hardware, and time?
- How much is performance reproducibility worth?
- What replaces “node hours” for user accounting and prioritization?
- How much complexity/choice should be revealed to users?
- How to we guarantee safe operation?