

Semiconductor Industry Energy Efficiency Scaling (EES2) Working Group Meeting #4

Hosted by the DOE/EERE Advanced Materials and Manufacturing Technologies Office (AMMTO)
SLAC National Accelerator Laboratory
Trinity Conference Room (Room 1350), Building 53

AGENDA

March 15, 8:30 AM – 5:00 PM PDT	
Time	Activity
8:30 – 11:20	Opening Plenary
8:30 – 8:40	Welcome from SLAC (Paul McIntyre, Associate Lab Director, SLAC)
8:40 – 8:55	Welcome from DOE AMO Organizer (Tina Kaarsberg, DOE AMMTO, Meeting Chair)
8:55 – 9:10	Pledge Signing for New Signers
9:10 – 9:30	Hierarchical Energy Usage (Sadas Shankar, Stanford/SLAC)
9:30 – 10:30	Plenary Talks <ul style="list-style-type: none"> • Bobbie Manne, Senior Fellow on Efficiency and Sustainability, AMD • Steve Pawlowski, Vice President Advanced Computing Solutions, Micron • Rene Haas, CEO, ARM
10:30 – 11:00	Combined Q&A
11:00 – 11:10	Roadmap Strategy and Day 1 Overview
11:10 – 11:25	BREAK
11:25AM – 4:35PM	Facilitated Discussions
11:25AM – 12:50PM	Parallel Session 1 <ul style="list-style-type: none"> • Trinity Room (Room 53-1350) – Advanced Packaging Heterogeneous Integration • Havasu (Room 53-3004) – Power and Control • Tulare (Room 53-4006) – Metrology and Benchmarking • Mammoth (Room 53-3036) – Manufacturing Energy Efficiency and Sustainability
12:50 – 1:35	LUNCH (Provided by Micron)
1:35 – 3:00	Parallel Session 2 <ul style="list-style-type: none"> • Trinity Room (Room 53-1350) – Materials and Devices • Havasu (Room 53-3004) – Software and Algorithms • Tulare (Room 53-4006) – Circuits and Architectures • Mammoth (Room 53-3036) – Manufacturing Energy Efficiency and Sustainability
3:00 – 3:15	BREAK

3:15 – 4:35	Cross-Collaboration: WG Speed Dating <ul style="list-style-type: none"> WG's to rotate between rooms
4:35 – 4:50	Day 2 Expectations
4:50 – 5:00	Day 1 Closing Remarks (Tina Kaarsberg, DOE AMMTO, Meeting Chair)

March 16, 9:00 AM – 3:30 PM PDT	
Time	Activity
9:00 – 9:45	Opening Plenary
9:00 – 9:15	Welcome from DOE AMO Organizer (Tina Kaarsberg, DOE AMMTO, Meeting Chair)
9:15 – 9:45	Materials and Devices Working Group Highlight (John Baniecki, SLAC)
9:45 – 9:55	Day 2 Overview
9:55 – 10:05	BREAK
10:05AM – 3:05PM	Facilitated Discussions
10:05 – 11:25	Parallel Session 1 <ul style="list-style-type: none"> Trinity Room (Room 53-1350) – Advanced Packaging Heterogeneous Integration Havasu (Room 53-3004) – Power and Control Tulare (Room 53-4006) – Metrology and Benchmarking Mammoth (Room 53-3036) – Manufacturing Energy Efficiency and Sustainability
11:25AM – 12:10PM	LUNCH (Purchase at SLAC Café)
12:10 – 1:30	Parallel Session 2 <ul style="list-style-type: none"> Trinity Room (Room 53-1350) – Materials and Devices Havasu (Room 53-3004) – Software and Algorithms Tulare (Room 53-4006) – Circuits and Architectures Mammoth (Room 53-3036) – Manufacturing Energy Efficiency and Sustainability
1:30 – 1:40	BREAK
1:40 – 3:05	Cross-Collaboration: WG Speed Dating <ul style="list-style-type: none"> WG's to rotate between rooms
3:05 – 3:20	WG Meeting #5 Preview and Homework
3:20 – 3:30	Day 2 Closing Remarks (Tina Kaarsberg, DOE AMMTO, Meeting Chair)